

ABSTRACT OF THE DISCLOSURE

A power fluctuation inhibiting device for effectively
inhibiting fluctuation of power supply voltage that is
5 supplied to internal circuits. The inhibiting device
includes a power fluctuation measuring circuit for measuring
peaks in the fluctuation of the power supply voltage
produced when the internal circuits are operated. A clock
signal control circuit adjusts phases of clock signals
10 provided to respective ones of the internal circuits in
accordance to the peak measuring result to substantially
offset the fluctuation peaks produced when the internal
circuits are operated.